

Computer Architecture MCQ Question Answer

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Question: 1. During the execution of the instructions, a copy of the instructions is placed in the _____ .

- Option A. Register
- Option B. RAM
- Option C. System heap
- Option D. Cache

Question: 2. The main virtue for using single Bus structure is ,

- Option A. Cost effective connectivity and speed
- Option B. Fast data transfers
- Option C. Cost effective connectivity and ease of attaching peripheral devices
- Option D. None of these

Question: 3. Two processors A and B have clock frequencies of 700 Mhz and 900 Mhz respectively. Suppose A can execute an instruction with an average of 3 steps and B can execute with an average of 5 steps. For the execution of the same instruction which processor is faster ?

- Option A. A
- Option B. B
- Option C. Both take the same time
- Option D. Insufficient information

Question: 4. _____ are used to over come the difference in data transfer speeds of various devices .

- Option A. Bridge circuits
- Option B. Speed enhancing circuitory
- Option C. Buffer registers
- Option D. Multiple Buses

Question: 5. A processor performing fetch or decoding of different instruction during the execution of another instruction is called _____ .

- Option A. Pipe-lining
- Option B. Super-scaling
- Option C. Parallel Computation
- Option D. None of these

Question: 6. To extend the connectivity of the processor bus we use _____ .

- Option A. SCSI bus
- Option B. PCI bus

- Option C. Multiple bus
- Option D. Controllers

Question: 7. ANSI stands for,

- Option A. American National Standard Interface
- Option B. American National Standards Institute
- Option C. American Network Standard Interfacing
- Option D. American Network Security Interrupt

Question: 8. An optimizing Compiler does,

- Option A. Better compilation of the given piece of code.
- Option B. Takes advantage of the type of processor and reduces its process time.
- Option C. Does better memory managment.
- Option D. Both a and c

Question: 9. IBM developed a bus standard for their line of computers PC AT called ____ .

- Option A. M-bus
- Option B. IB bus
- Option C. ISA
- Option D. None of these

Question: 10. The bus used to connect the monitor to the CPU is ____ .

- Option A. SCSI bus
- Option B. PCI bus
- Option C. Rambus
- Option D. Memory bus

Question: 11. The ultimate goal of a compiler is to,

- Option A. Reduce the size of the object code.
- Option B. Reduce the clock cycles for a programming task.
- Option C. Be able to detect even the smallest of errors.
- Option D. Be versatile.

Question: 12. ____ register Connected to the Processor bus is a single-way transfer capable .

- Option A. Temp
- Option B. PC
- Option C. Z
- Option D. IR

Question: 13. For a given FINITE number of instructions to be executed, which architecture of the processor provides for a faster execution ?

- Option A. Super-scalar

- Option B. ISA
- Option C. ANSA
- Option D. All of the above

Question: 14. In multiple Bus organisation, the registers are collectively placed and referred as _____ .

- Option A. Register file
- Option B. Set registers
- Option C. Register Block
- Option D. Map registers

Question: 15. When Performing a looping operation, the instruction gets stored in the _____ .

- Option A. Cache
- Option B. Registers
- Option C. System stack
- Option D. System Heap

Question: 16. The clock rate of the processor can be improved by,

- Option A. By using overclocking method
- Option B. Improving the IC technology of the logic circuits
- Option C. Reducing the amount of processing done in one step
- Option D. All of the above

Question: 17. The main advantage of multiple bus organisation over single bus is,

- Option A. Increase in size of the registers
- Option B. Reduction in the number of cycles for execution
- Option C. Better Connectivity
- Option D. None of these

Question: 18. As of 2000, the reference system to find the performance of a system is _____ .

- Option A. None of these
- Option B. Ultra SPARC 10
- Option C. SUN II
- Option D. SUN SPARC

Question: 19. The ISA standard Buses are used to connect,

- Option A. GPU and processor
- Option B. RAM and processor
- Option C. CD/DVD drives and Processor
- Option D. Harddisk and Processor

Question: 20. SPEC stands for,

- Option A. Standard Performance Evaluation Code.
- Option B. Standard Processing Enhancement Corporation.
- Option C. System Processing Enhancing Code.
- Option D. System Performance Evaluation Corporation.

Question: 21. The ascending order or a data Hierarchy is

- Option A. bytes - bit- field - record - file - database
- Option B. bit - bytes - record - field - file - database
- Option C. bytes -bit - record - field - file - database
- Option D. bit - bytes - fields - record - file - database

Question: 22. In immediate addressing the operand is placed

- Option A. after OP code in the instruction
- Option B. in memory
- Option C. in stack
- Option D. in the CPU register

Question: 23. How many address lines are needed to address each memory locations in a 2048 x 4 memory chip?

- Option A. 8
- Option B. 10
- Option C. 11
- Option D. 12

Question: 24. A computer program that converts an entire program into machine language at one time is called a/an

- Option A. simulator
- Option B. interpreter
- Option C. commander
- Option D. compiler

Question: 25. Interrupts which are initiated by an I/O drive are

- Option A. internal
- Option B. external
- Option C. software
- Option D. all of above

Question: 26. When the RET instruction at the end of subroutine is executed,

- Option A. the memory address of the RET instruction is transferred to the program counter
- Option B. the information where the stack is initialized is transferred to the stack pointer
- Option C. two data bytes stored in the top two locations of the stack are transferred to the stack pointer
- Option D. two data bytes stored in the top two locations of the stack are transferred to the

program counter

Question: 27. A microporgram is sequencer perform the operation

Option A. read

Option B. read and execute

Option C. execute

Option D. write

Question: 28. The ALU and control unit of most of the microcomputers are combined and manufacture on a single silicon chip. What is it called?

Option A. microprocessor

Option B. ALU

Option C. monochip

Option D. control unit

Question: 29. Microprocessor 8085 can address location upto

Option A. 32K

Option B. 128K

Option C. 64K

Option D. 1M

Answer Sheet

1	D	
2	C	
3	A	
4	C	
5	A	
6	B	
7	B	
8	B	
9	C	
10	A	
11	B	
12	C	
13	A	
14	A	
15	A	
16	D	
17	B	
18	B	
19	D	
20	D	
21	D	
22	A	
23	C	
24	D	
25	B	
26	D	
27	B	
28	A	
29	C	